



A cross-sectional view of a substrate 66. A conductive layer 52 is formed on the top surface of the substrate 66. The conductive layer 52 is divided into three rectangular regions by two gaps 64. The gaps 64 are filled with a dielectric material 84. A label 80 points to the left edge of the substrate 66.

**FIG. 11**

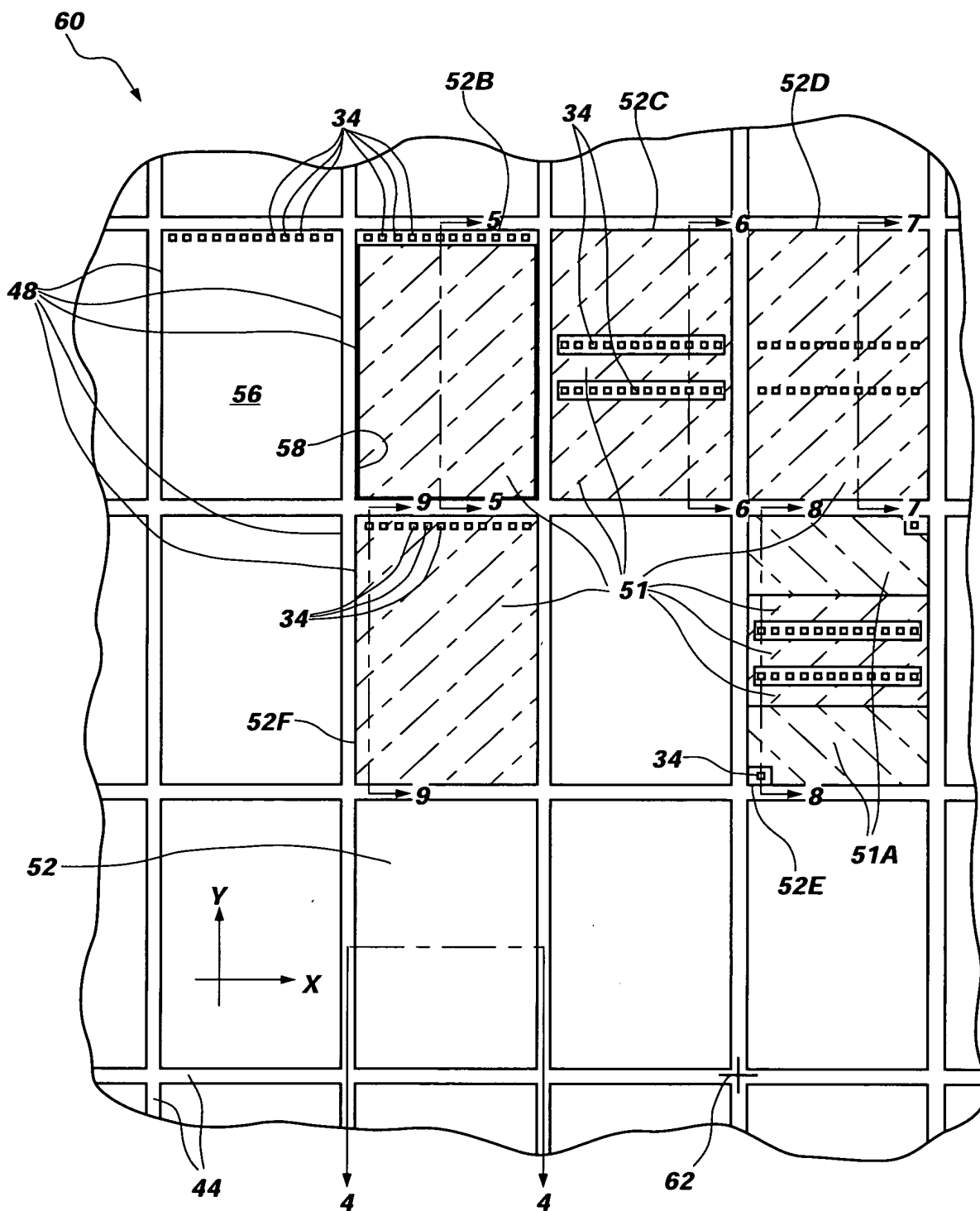
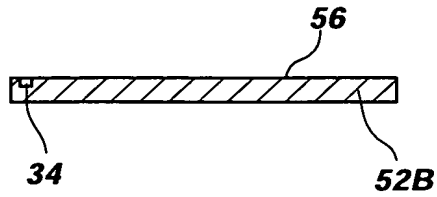
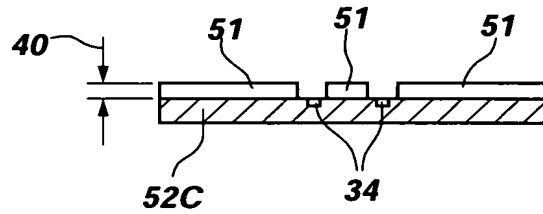


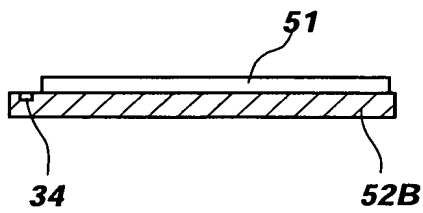
FIG. 3



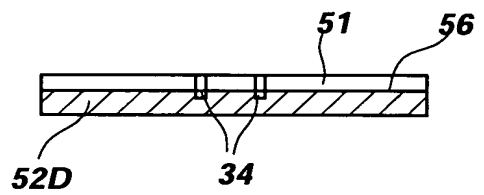
**FIG. 4**



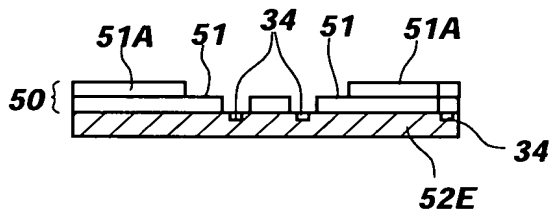
**FIG. 6**



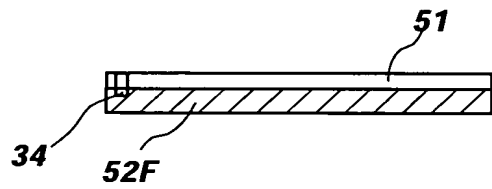
**FIG. 5**



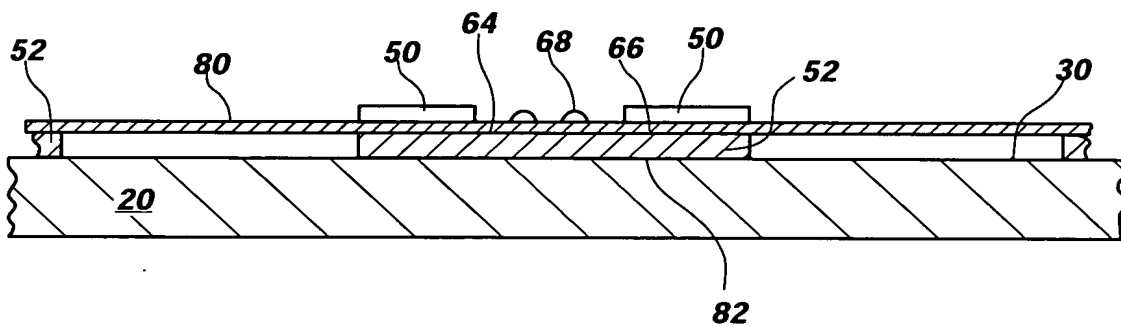
**FIG. 7**



**FIG. 8**



**FIG. 9**



**FIG. 10**



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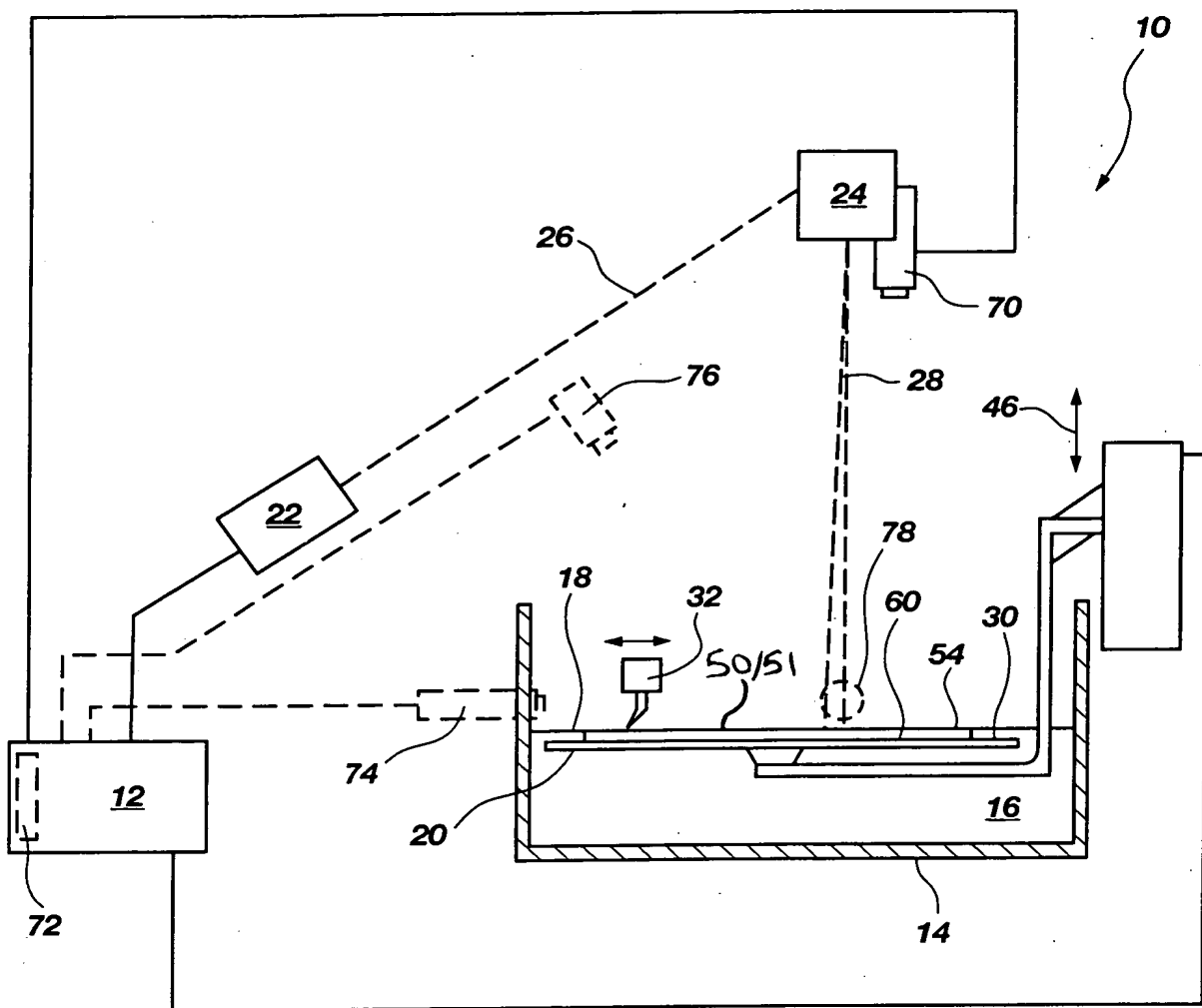


FIG. 1

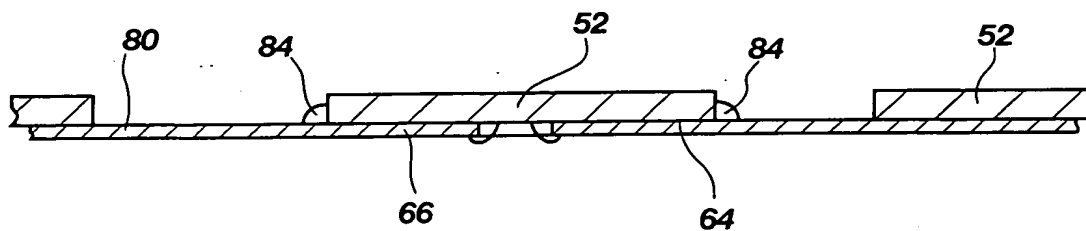


FIG. 11



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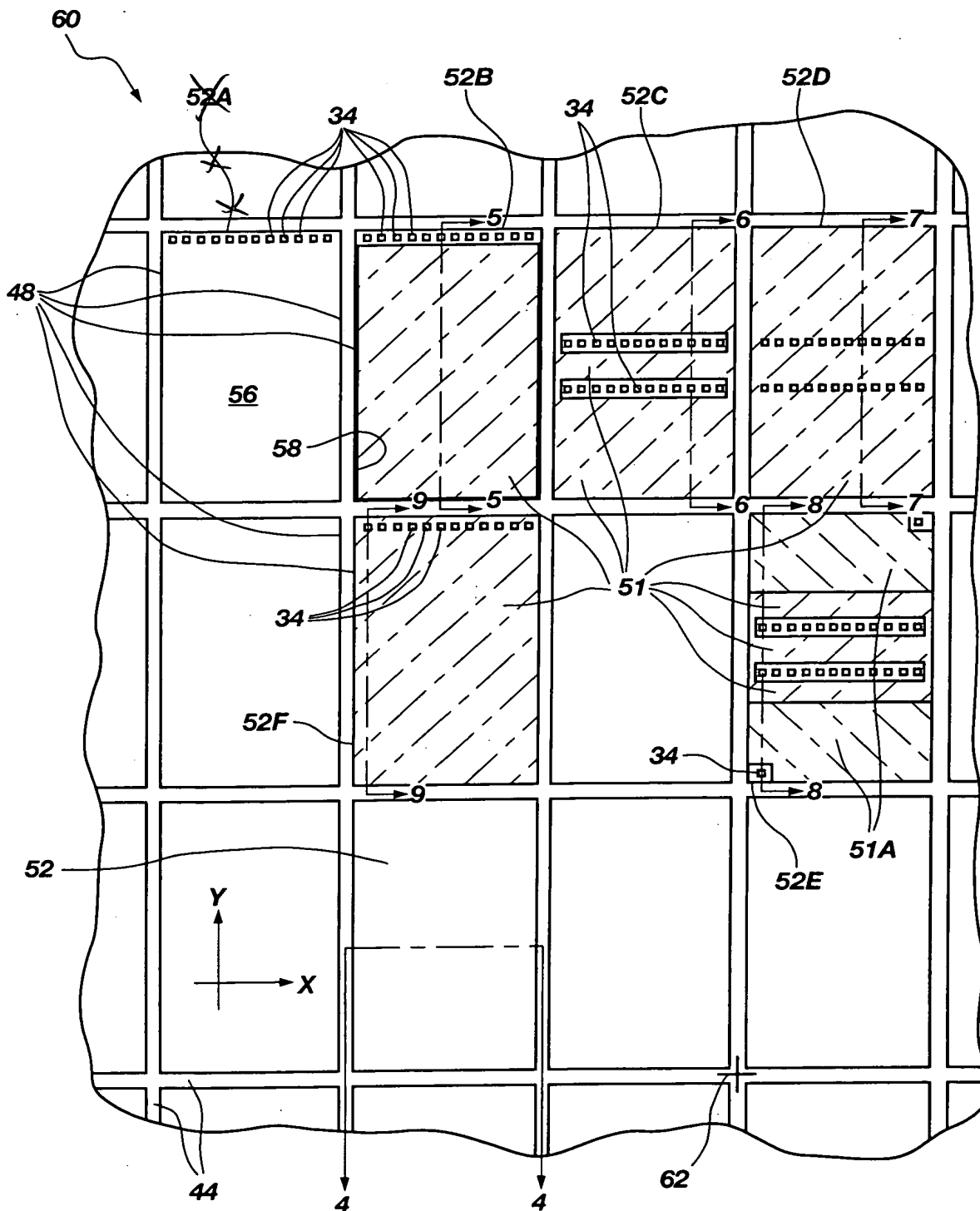


FIG. 3



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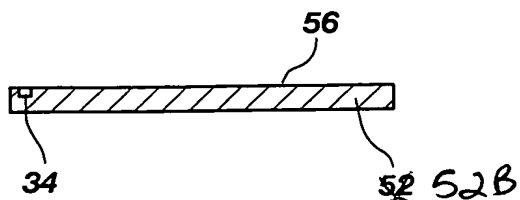


FIG. 4

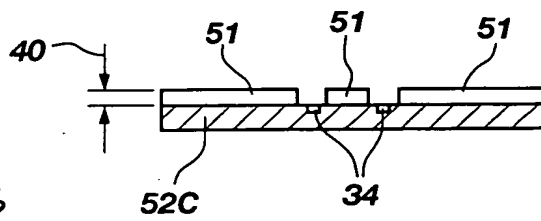


FIG. 6

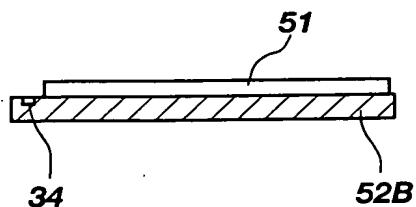


FIG. 5

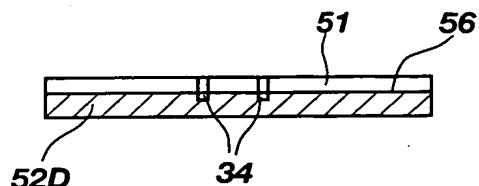


FIG. 7

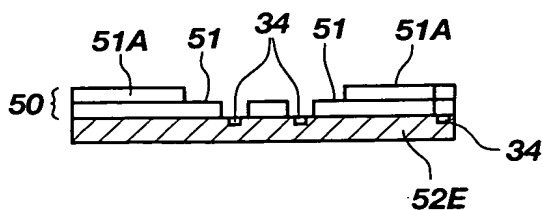


FIG. 8

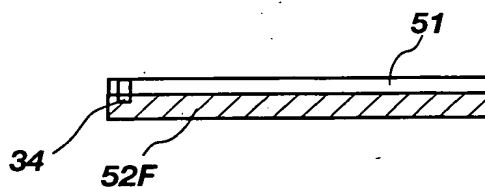


FIG. 9

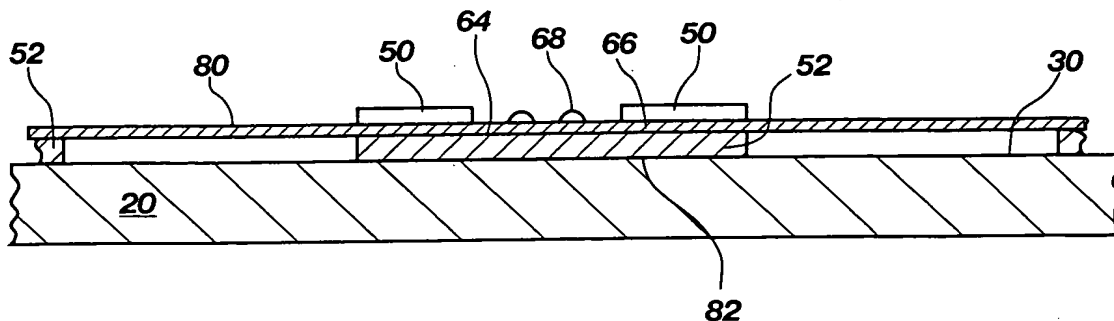


FIG. 10